ARIES Software Readiness

Papyrus

Theories and Designs
- TRL 1-2

Research prototypes
- TRL 2-5

OpenACC2FPGA
- NVL-C
- IMPACC
- HLIR
- HLIR2LLVM
- FITL
- OpenARC
- HLIR

Various demonstrations on DOE miniapps and some full apps

Application demonstrations
- TRL 5-6

Production software
- TRL 8-9

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